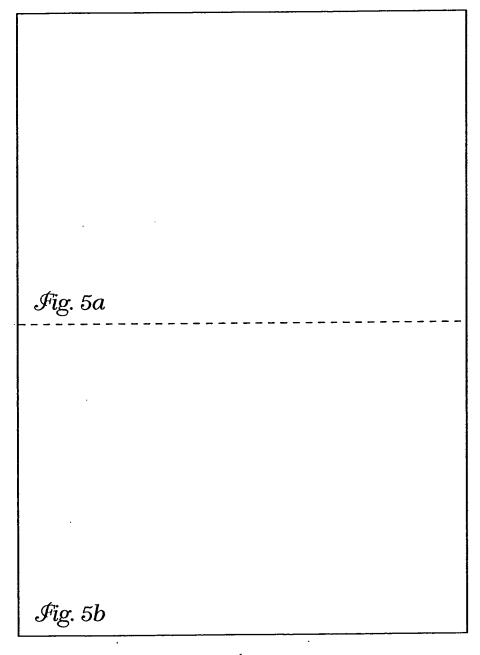
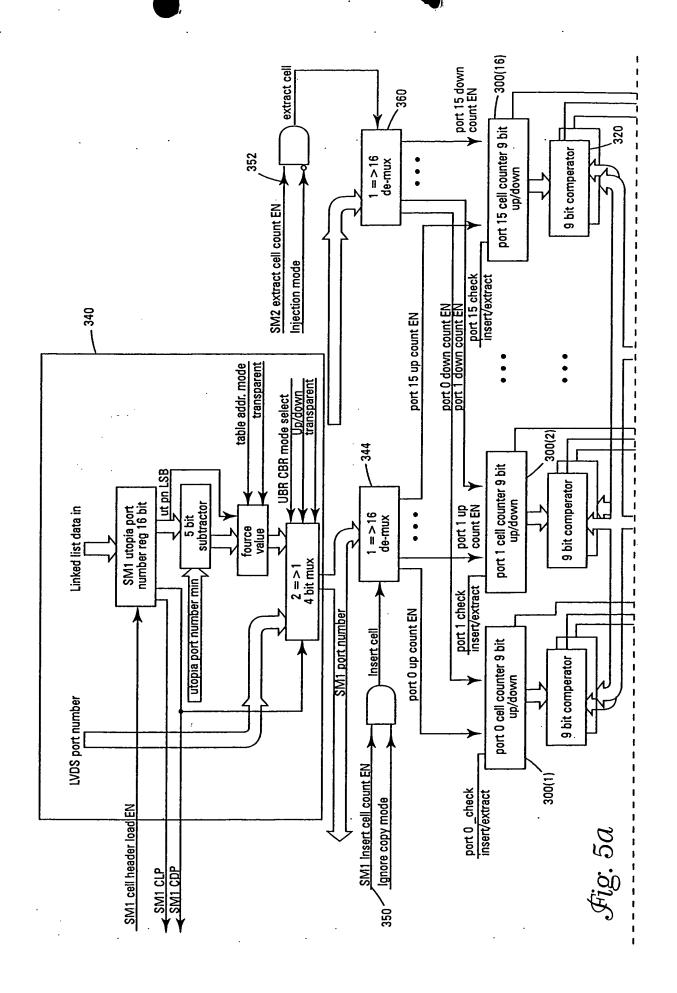
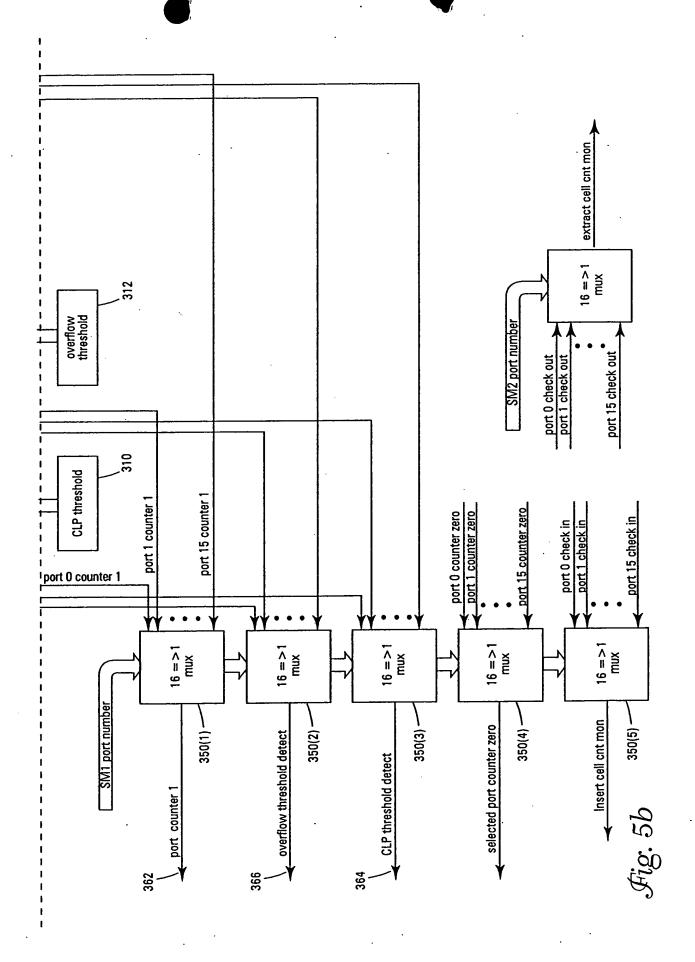
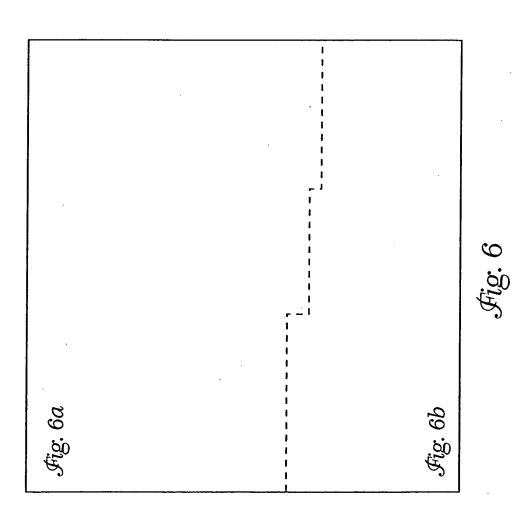


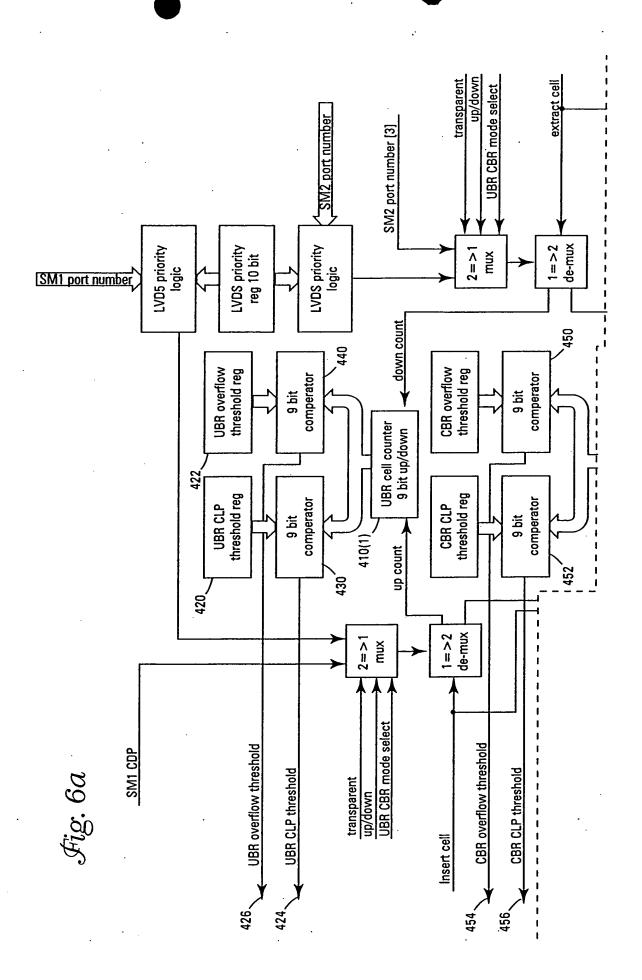
Fig. 4

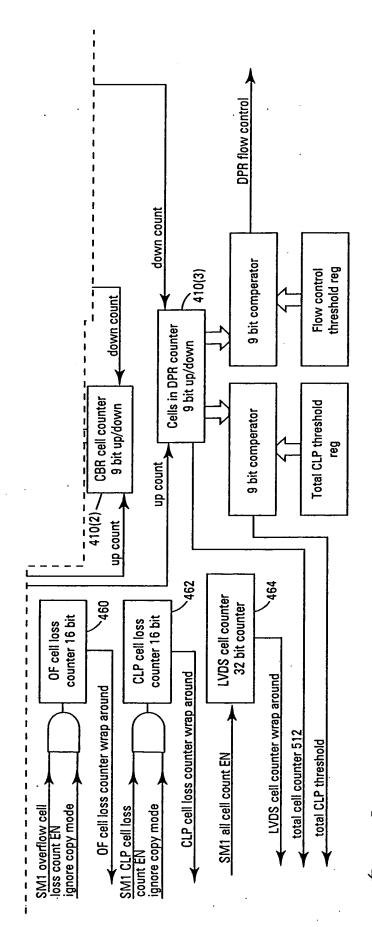








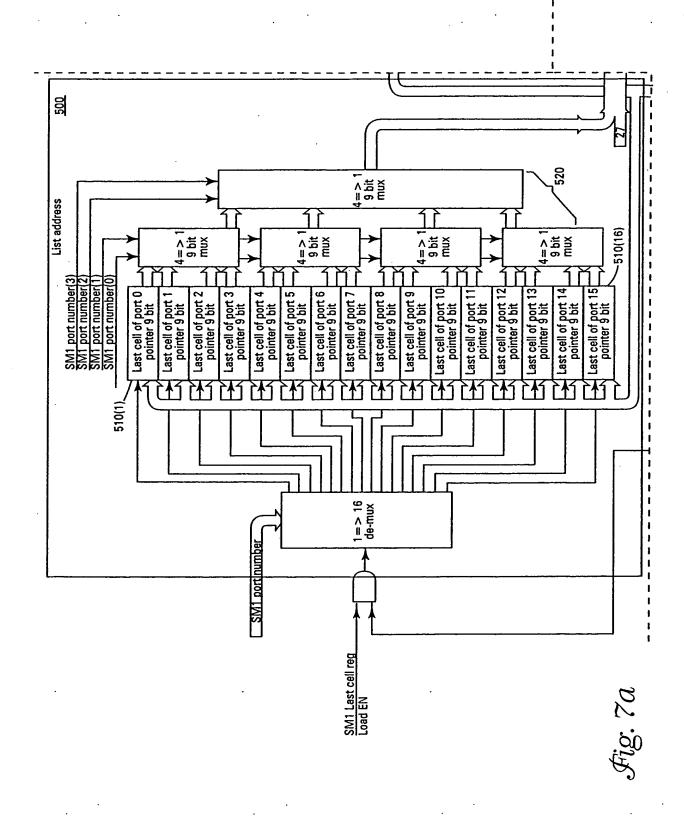


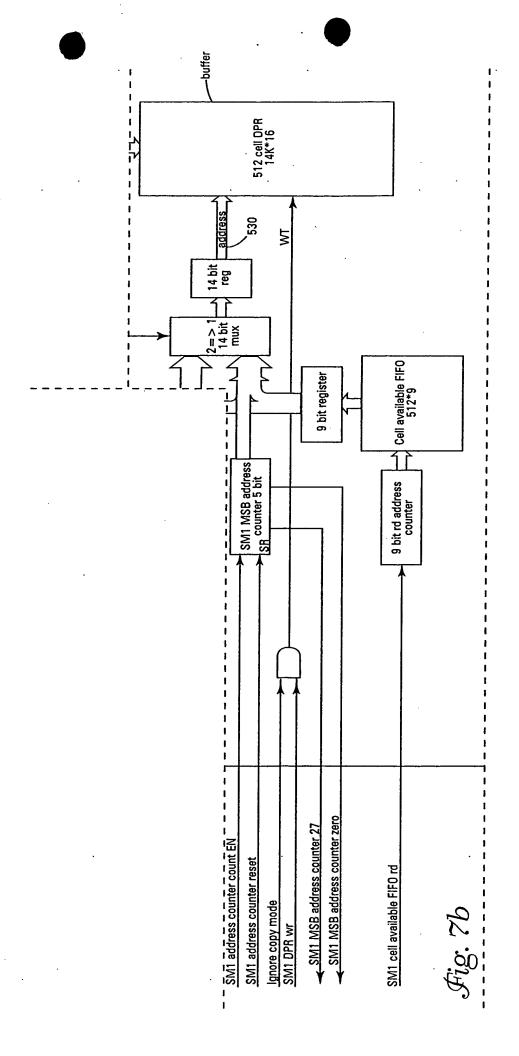


 \mathscr{F} ig. 6b

	Fig. 7d
	_
	1
	i I
	! !.
	.
Fig. 7a	
	-
Fig. 7b	
Fig. 7 c	

Fig. 7





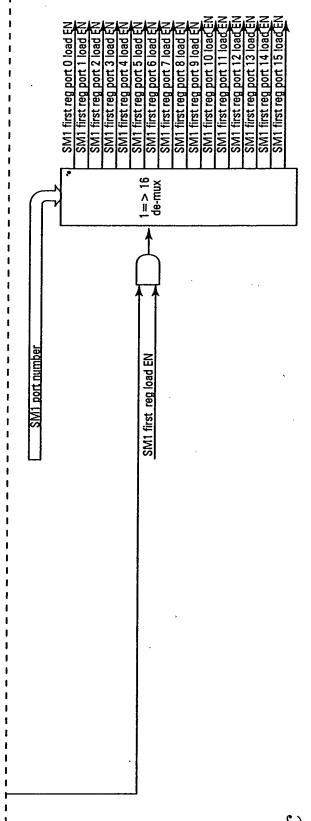


Fig. 7c

Data in

VPI read strobe

SM1 UDF read strobe

SM1 mux control

SM1 mux control

SM1 mux control

SM1 mux control

SM2

Fig. 7d

table addr mode
transparent

insert cell
6 bit mux

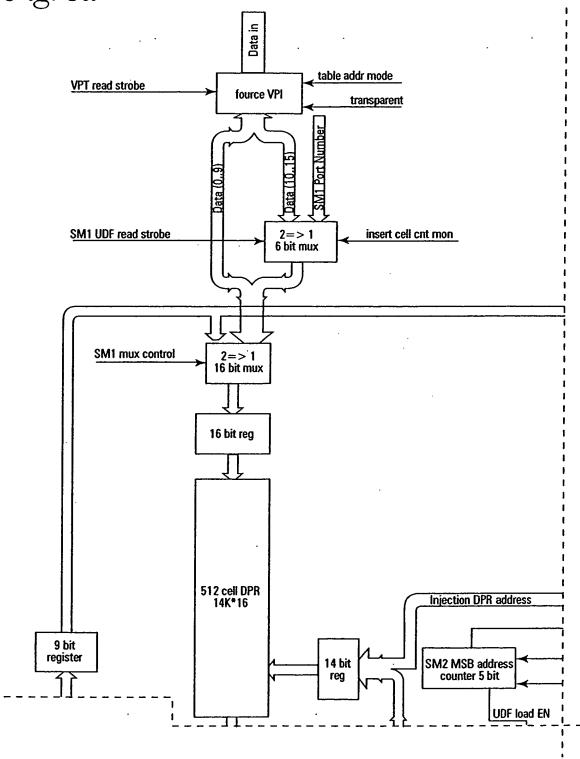
first pointer update

16 bit reg

	· · · · · · · · · · · · · · · · · · ·	•
Fig. 8a		Fig. 8c
	; ;	
	! ! !	
	; ; ;	
	! ! !	
	! ! !	
	1 1 1	
	! ! 	
•	!	·
·	 	
	: : :	
	! !	
	1 	
Fig. 8b	! ! !	Fig. 8d
5 15.00	t	o 18. 00

Fig. 8

Fig. 8a



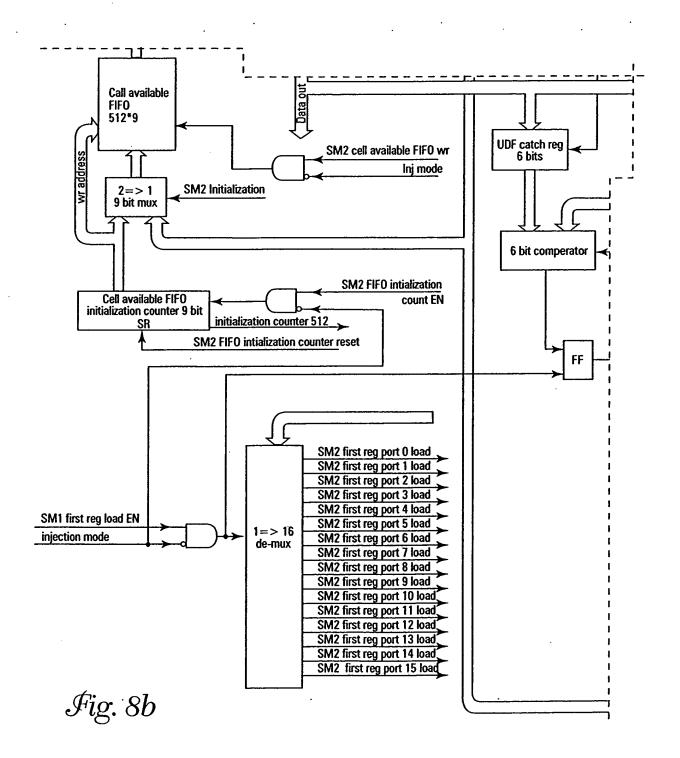
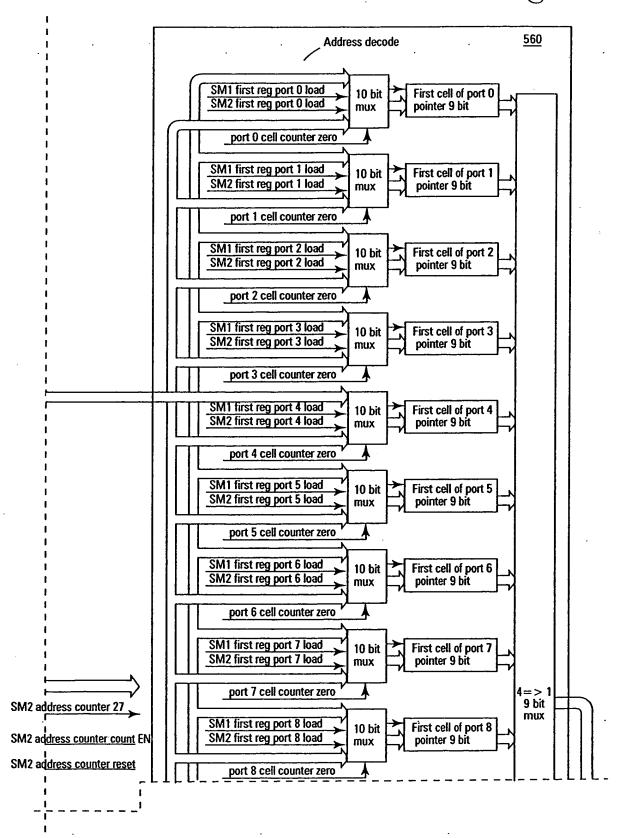


Fig. 8c



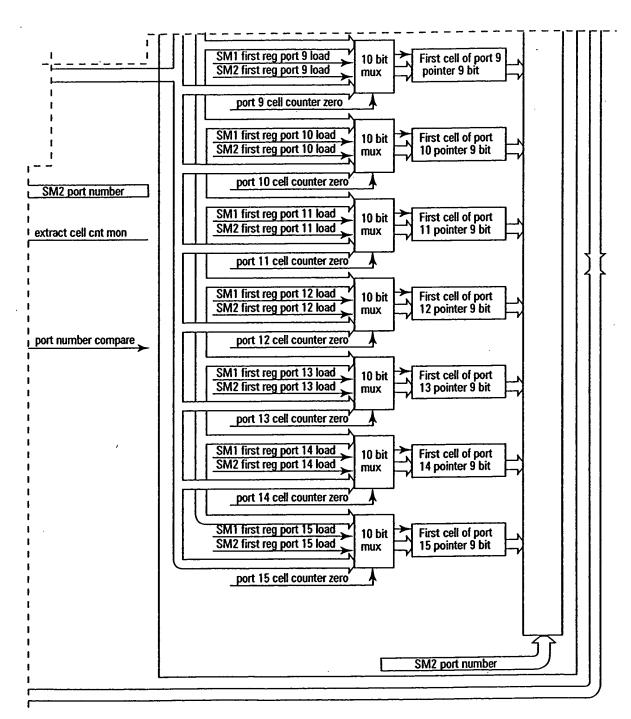


Fig. 8d